

SYSTEM AND METHOD FOR SHARED USE OF A COMMON GPIO LINE

Abstract of the Disclosure

Disclosed herein are a method and system for sharing a single GPIO line of an integrated circuit (IC) between at least two circuit components connected concurrently, wherein at least one of the circuit components is to provide input via the GPIO line and at least one circuit component is to receive output via the GPIO line. The input and output can be provided at separate times with the status of the GPIO line changing accordingly or, provided that the input is provided at a lower frequency relative to the switching frequency of the GPIO line, an input to the IC can be provided concurrent with an output from the IC since the low frequency input typically persists until an input cycle of the GPIO line. The present invention finds particular benefit when implemented to interface with GPIO lines of a microprocessor.

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